

Vishay Siliconix

P-Channel 30-V (D-S) MOSFET

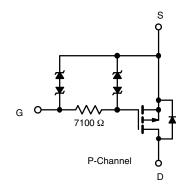
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 30	0.0085 at V _{GS} = - 10 V	- 14		
	0.014 at V _{GS} = - 4.5 V	- 11		

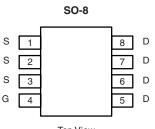
FEATURES

- Halogen-free According to IEC 61249-2-21
 Available
- TrenchFET[®] Power MOSFET
- ESD Protection: 3000 V

APPLICATIONS

- Notebook PC
 - Load Switch
 - Adapter Switch





Top View

Ordering Information: Si4483EDY-T1-E3 (Lead (Pb)-free) Si4483EDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 30		V
Gate-Source Voltage		V _{GS}	± 25		
Continuous Dusin Quarant (T. 150 %0)	T _A = 25 °C	– I _D	- 14	- 10	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 11	- 8	•
Pulsed Drain Current		I _{DM}	- 50		A
Continuous Source Current (Diode Conduction) ^a		I _S	- 2.7	- 1.36	
	T _A = 25 °C	Р	3.0	1.5	10/
Maximum Power Dissipation ^a	T _A = 70 °C	P _D	1.9	0.95	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marian Incline to Anthian Id	t ≤ 10 s	- R _{thJA} R _{thJF}	33	42	
Maximum Junction-to-Ambient ^a	Steady State		70	85	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		16	21	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



HALOGEN

Available

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1.0		- 3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$			± 1	μA	
		$V_{DS} = 0 V, V_{GS} = \pm 25 V$			± 10	mA	
Zero Gate Voltage Drain Current	I _{DSS} –	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	- 1		- 1		
		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 70 °C			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	- 30			А	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 14 A		0.007	0.0085		
		V _{GS} = - 4.5 V, I _D = - 11 A		0.0115	0.014	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 14 A		60		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = -2.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.74	- 1.1	V	
Dynamic ^b			•				
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		20	30		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 10 V, R_g = 6 Ω		42	65	μs	
Fall Time	t _f			50	80	1	

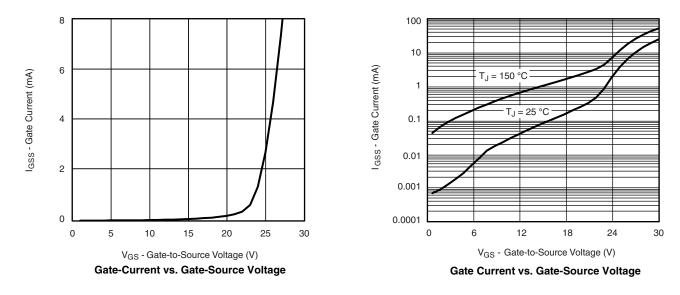
Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

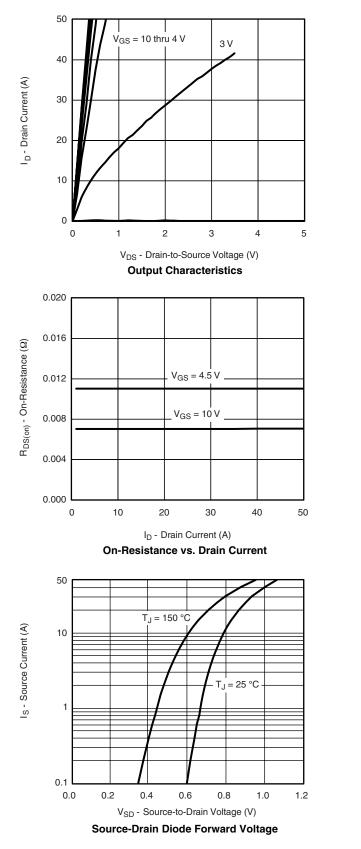
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

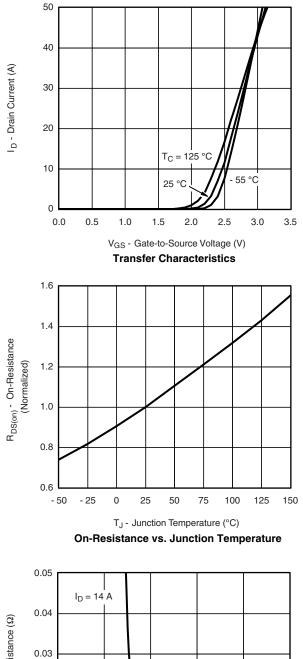


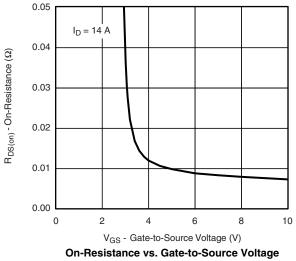


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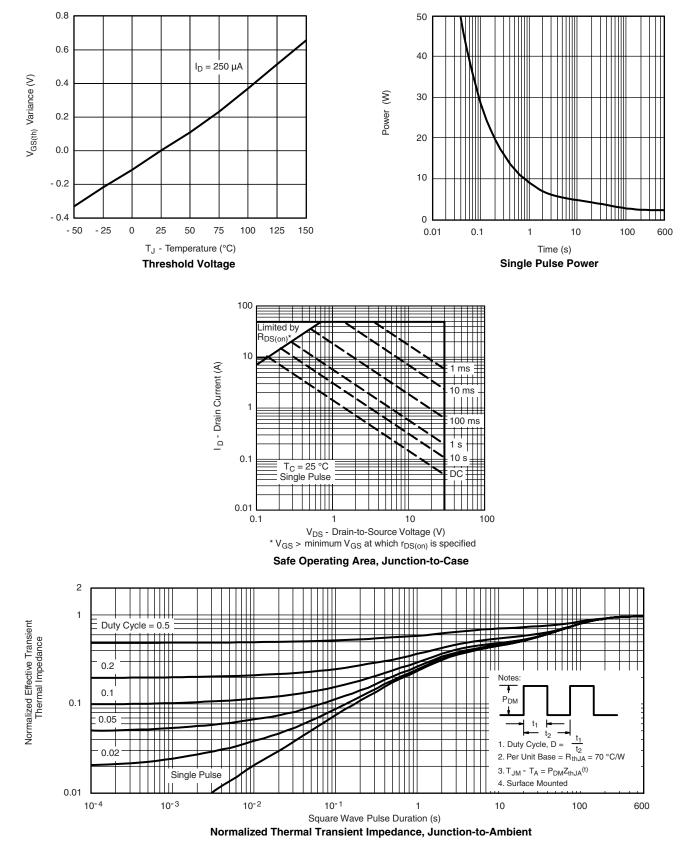




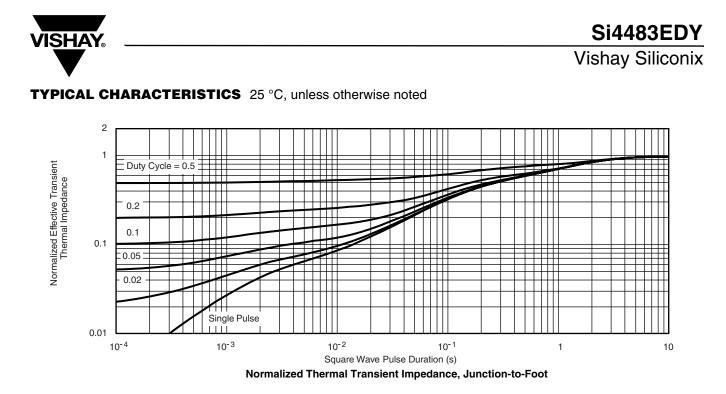
Si4483EDY

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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72862.



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