() IDT.

QUICKSWITCH[®] PRODUCTS HIGH-SPEED CMOS QUICKSWITCH DUAL 4:1 MUX/DEMUX

FEATURES:

- · Enhanced N channel FET with no inherent diode to Vcc
- 5 Ω bidirectional switches connect inputs to outputs
- Pin compatible with the 74F253, 74FCT253, and 74FCT253T
- · Zero propagation delay, zero ground bounce
- · Undershoot clamp diodes on all switch and control inputs
- TTL-compatible control inputs
- Available in QSOP and S1 packages

DESCRIPTION:

The QS3253 is a high-speed CMOS TTL-compatible dual 4:1 multiplexer/demultiplexer with 3-state outputs. The QS3253 is function and pinout compatible version of the 74F253, 74FCT253, and the 74ALS/AS/LS253 dual 4:1 multiplexers. The low ON resistance of the QS3253 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise.

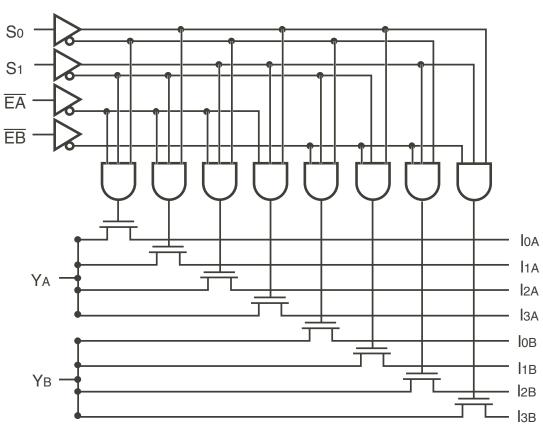
Mux/Demux devices provide an order of magnitude faster speed than equivalent logic devices.

The QS3253 is characterized for operation at -40°C to +85°C.

APPLICATIONS:

- Logic replacement
- · Video, audio, graphics switching, muxing
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Power conservation
- Bus funneling

FUNCTIONAL BLOCK DIAGRAM



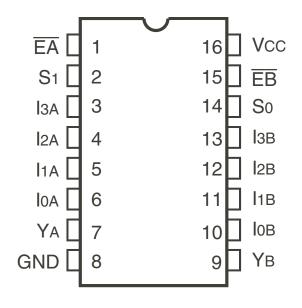
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INDUSTRIAL TEMPERATURE RANGE

FEBRUARY 2006

1

PIN CONFIGURATION



QSOP/S1 **TOP VIEW**

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	–0.5 to +7	V
VTERM ⁽³⁾	DC Switch Voltage Vs	–0.5 to +7	V
VTERM ⁽³⁾	DC Input Voltage VIN	–0.5 to +7	V
VAC	AC Input Voltage (pulse width \leq 20ns)	-3	V
Ιουτ	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (TA = 85°C)	0.5	W
Tstg	Storage Temperature	–65 to +150	°C

NOTE:

1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

2. Vcc terminals.

3. All terminals except Vcc.

CAPACITANCE (TA = +25°C, f = 1MHz, VIN = 0V, VOUT = 0V)						
Pins		Тур.	Max. ⁽¹⁾	Unit		
Control Inputs		4	5	рF		
Quickswitch Channels	Demux	5	7	рF		
(Switch OFF)	Mux	14	16			

NOTE:

1. This parameter is guaranteed but not production tested.

PIN DESCRIPTION

Pin Names	I/O	Description
Ixx	I	Data Inputs
S0 - S2	I	Select Inputs
EA, EB	I	Enable Input
YA, YB	0	Data Output

FUNCTION TABLE(1)

Enable		Select		Outputs		
ĒĀ	ĒB	S 1	S0	YA	Υв	Function
Н	Х	Х	Х	Hi-Z	Х	Disconnected
Х	Н	Х	Х	Х	Hi-Z	Disconnected
L	L	L	L	l0A	Юв	S1-0=0
L	L	L	Н	l1A	l1b	S1 - 0 = 1
L	L	Н	L	l2A	l2B	S1-0=2
L	L	Н	Н	I3A	Ізв	S1 - 0 = 3

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

X = Don't Care

Z = High-Impedence

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified: Industrial: TA = -40°C to +85°C, Vcc = $5.0V \pm 5\%$

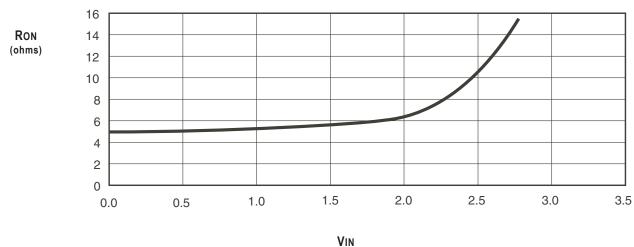
Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
Vih	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	—	_	V
VIL	Input LOW Level	Guaranteed Logic LOW for Control Pins	_	—	0.8	V
lin	Input LeakageCurrent (Control Inputs)	$0V \le VIN \le VCC$	_	—	±1	μA
loz	Off-State Output Current (Hi-Z)	$0V \le VOUT \le VCC$	_	—	±1	μA
Ron	Switch ON Resistance	Vcc = Min., VIN = 0V, ION = 30mA	_	5	7	Ω
		Vcc = Min., VIN = 2.4V, ION =15mA	—	10	15	
Vp	Pass Voltage ⁽²⁾	$V_{IN} = V_{CC} = 5V$, Iout = $-5\mu A$	3.7	4	4.2	V

NOTES:

1. Typical values are at Vcc = 5.0V, TA = 25° C.

2. Pass Voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs Vin AT Vcc = 5V



(Volts)

POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
lccq	Quiescent Power Supply Current	Vcc = Max., VIN = GND or Vcc, f = 0	3	μA
Δlcc	Power Supply Current per Control Input HIGH (2)	Vcc = Max., VIN = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz ⁽³⁾	Vcc = Max., I and Y pins open	0.25	mA/MHz
		Control Inputs Toggling at 50% Duty Cycle		

NOTES:

1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.

2. Per TLL driven input (VIN = 3.4V, control inputs only). I and Y pins do not contribute to ∆lcc.

3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40^{\circ}C$ to +85°C, Vcc = 5.0V ± 5%;

CLOAD = 50pF, RLOAD = 500Ω unless otherwise noted.

Symbol	Parameter	Min. ⁽¹⁾	Тур.	Max.	Unit
tPLH	Data Propagation Delay ^(2,3)	—	—	0.25	ns
tPHL	Ix to Y				
tPZL	Switch Turn-on Delay	0.5	—	6.6	ns
tРZH	Sx to Y				
tPZL	Switch Turn-on Delay	0.5	—	6	ns
tРZH	Ex to Y				
tPLZ	Switch Turn-off Delay ⁽²⁾	0.5	—	6	ns
tPHZ	\overline{Ex} to Y, Sx to Y				

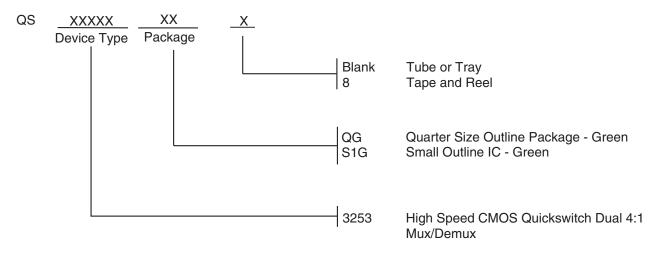
NOTES:

1. Minimums are guaranteed but not production tested.

2. This parameter is guaranteed but not production tested.

^{3.} The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for CL = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



Datasheet Document History

Pg. 5

02/14/2011

Updated the ordering information by removing the "IDT" notation, non RoHS part and by adding Tape and Reel information.



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IDT (Integrated Device Technology): QS3253QG QS3253S1G QS3253QG8 QS3253S1G8