



5 Vcc

4 Y

#### SINGLE 2 INPUT POSITIVE EXCLUSIVE OR GATE

(Top View)

**SOT 353** 

### Description

The 74LVC1G86Q is an automotive-compliant, single 2-input positive EXCLUSIVE OR gate with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down.

The gate performs the positive Boolean function:

 $Y = A \oplus B$  or  $Y = \overline{A}B + A\overline{B}$ 

#### Features

- Grade 1 Ambient Temperature Operation: -40°C to 125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial Power Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per AEC-Q100
  - Exceeds 2000V Human Body Model (AEC Q100-002)
  - Exceeds 1000V Charged Device Model (AEC Q100-011)
- Latch-Up Exceeds 100mA (AEC Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G86Qis suitable for automotive applications requiring specific change control and is AEC-Q100 qualified, has a grade 1 -40°C to 125°C temperature rating, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

#### Applications

• Voltage Level Shifting

**Pin Assignments** 

A | 1

B | 2

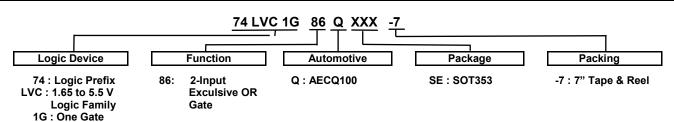
GND 3

- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products such as:
  - Automotive Applications Within Grade 1 Temperature Range
  - Industrial Computing/Controls/Automation
  - High Reliability Networking/Communications
  - Industrial/Agricultural Equipment

- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



#### Ordering Information (Note 4)



Part Number	Package	Package	Package	7" Tape ar	nd Reel
Fart Number	Code	(Notes 6 & 7)	Size	Quantity	Part Number Suffix
74LVC1G86QSE-7	SE	SOT353	2.0mm × 2.0mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7

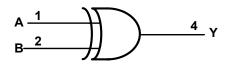
Notes: 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

5. Pad layout as shown in Diodes Inc. suggested pad layouts, which can be found on our website at see http://www.diodes.com/package-outlines.html. 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Pin Descriptions**

Pin Name	Description
А	Data Input
В	Data Input
GND	Ground
Y	Data Output
V <sub>CC</sub>	Supply Voltage

# Logic Diagram



# **Function Table**

Inp	Output	
Α	В	Y
Н	Н	L
L	Н	Н
Н	L	Н
L	L	L



### Absolute Maximum Ratings (Notes 7 & 8)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V <sub>CC</sub> +0.5	V
Ік	Input Clamp Current VI<0	-50	mA
Іок	Output Clamp Current	-50	mA
Ι <sub>Ο</sub>	Continuous Output Current	±50	mA
I <sub>CC,</sub> I <sub>GND</sub>	Continuous Current Through V <sub>CC</sub> or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

Notes: 7. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

Symbol		Parameter	Min	Max	Unit	
N/		Operating	1.65	5.5	V	
V <sub>CC</sub>	Operating Voltage	Data Retention Only	1.5	—	V	
		V <sub>CC</sub> = 1.65V to 1.95V	0.65 × V <sub>CC</sub>	_		
		V <sub>CC</sub> = 2.3V to 2.7V	1.7	—	V	
VIH	High-Level Input Voltage	V <sub>CC</sub> = 3V to 3.6V	2	—	V	
		V <sub>CC</sub> = 4.5V to 5.5V	0.7 × V <sub>CC</sub>	—		
		V <sub>CC</sub> = 1.65V to 1.95V	_	0.35 × V <sub>CC</sub>		
		V <sub>CC</sub> = 2.3V to 2.7V	_	0.7	.,	
V <sub>IL</sub>	Low-Level Input Voltage	V <sub>CC</sub> = 3V to 3.6V	_	0.8	V	
		V <sub>CC</sub> = 4.5V to 5.5V	_	0.3 × V <sub>CC</sub>		
VI		Input Voltage		5.5	V	
Vo		Output Voltage	0	Vcc	V	
		V <sub>CC</sub> = 1.65V	_	-4		
	High-Level Output Current	V <sub>CC</sub> = 2.3V	—	-8	mA	
		V <sub>CC</sub> = 2.7V	—	-12		
lон			—	-16		
			V <sub>CC</sub> = 3V	—	-24	
		V <sub>CC</sub> = 4.5V	—	-32	l	
		V <sub>CC</sub> = 1.65V	—	4		
		V <sub>CC</sub> = 2.3V	—	8	l	
	Low-Level Output Current	V <sub>CC</sub> = 2.7V	—	12	m۸	
I <sub>OL</sub>	Low-Level Output Current		_	16	mA	
		V <sub>CC</sub> = 3V	—	24		
		$V_{CC} = 4.5V$	—	32		
		$V_{CC}$ = 1.8V ± 0.15V, 2.5V ± 0.2V	—	20		
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	—	10	ns/V	
		$V_{CC} = 5V \pm 0.5V$	—	5		
T <sub>A</sub>	Operating Free-Air Temperature	-	-40	+125	°C	

# **Recommended Operating Conditions (Note 9)**

Note: 9. Unused inputs should be held at  $V_{CC}$  or Ground.



### **Electrical Characteristics** (All typical values are at V<sub>CC</sub> = 3.3V, T<sub>A</sub> = +25°C)

Quanta al	Devenuetor	Test C	u diti a u a	N	-40°	C to +125	°C	11	
Symbol	Parameter	Test Co	onditions	Vcc	Min	Тур	Мах	Unit	
			I <sub>OH</sub> = -100μA	1.65V to 5.5V	V <sub>CC</sub> -0.1	_	_		
			I <sub>OH</sub> = -4mA	1.65V	0.95	—	_		
V		$\lambda = \lambda = 0$	I <sub>OH</sub> = -8mA	2.3V	1.7	—	_	v	
V <sub>OH</sub>	High Level Output Voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$	I <sub>OH</sub> = -12mA	2.7V	1.9	—	—	v	
			I <sub>OH</sub> = -24mA	3V	2.0	—	_		
			I <sub>OH</sub> = -32mA	4.5V	3.4	—	—		
				I <sub>OL</sub> = 100μA	1.65V to 5.5V	—	—	0.10	
		VI = VIH or VIL	I <sub>OL</sub> = 4mA	1.65V	—	—	0.70	V	
			I <sub>OL</sub> = 8mA	2.3V	—	—	0.45		
V <sub>OL</sub>	Low Level Output Voltage		I <sub>OL</sub> = 12mA	2.7V	—	—	0.60		
			I <sub>OL</sub> = 24mA	3V	—	—	0.80		
			I <sub>OL</sub> = 32mA	4.5V	_	_	0.80		
lı –	Input Current	V <sub>I</sub> = 5.5V or GN	۱D	0 to 5.5V	—	±0.1	±1	μA	
I <sub>OFF</sub>	Power Down Leakage Current	$V_{\rm I}$ or $V_{\rm O}$ = 5.5V		0V	—	_	±2	μA	
I <sub>CC</sub>	Supply Current	V <sub>I</sub> = 5.5V or GND I <sub>O</sub> = 0		5.5V	—	0.1	4	μA	
ΔI <sub>CC</sub>	Additional Supply Current	One input at $V_{CC}$ –0.6V Other inputs at $V_{CC}$ or GND		3V to 5.5V	_	_	500	μA	
CI	Input Capacitance	$V_i$ = GND to $V_C$	C	3.3V	_	5.0		pF	

# Package Characteristics

Symbol	Parameter	Test Conditions	Vcc	Min	Тур.	Max	Unit
$\theta_{JA}$	Thermal Resistance Junction-to-Ambient	SOT353	Note 10	_	371	_	°C/W
θις	Thermal Resistance Junction-to-Case	SOT353	Note 10		143		°C/W

Note: 10. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

### **Switching Characteristics**

Figure 1 Typical Values at  $T_A = +25^{\circ}C$  and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Parameter	From	То	M <sub>2</sub> -	T <sub>A</sub> = -40°C to 125°C			Unit
Farameter	Input	Output	Vcc	Min	Тур	Max	Unit
	t <sub>PD</sub> A or B	B Y	1.8V ± 0.15V	1.0	3.7	13.0	
			2.5V ± 0.2V	0.5	2.5	7.0	ns
t <sub>PD</sub>			2.7V	0.5	2.8	7.5	
	-	3.3V ± 0.3V	0.5	2.3	6.5	-	
		5.0V ± 0.5V	0.5	1.9	5.5		

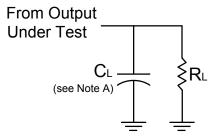
# **Operating Characteristics**

T<sub>A</sub> = +25°C

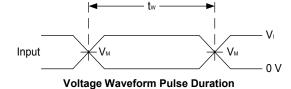
	Parameter	Test Conditions	V <sub>CC</sub> = 1.8V Typ	V <sub>CC</sub> = 2.5V Typ	V <sub>CC</sub> = 3.3V Typ	V <sub>CC</sub> = 5V Typ	Unit
C <sub>pd</sub>	Power Dissipation Capacitance	f = 10 MHz	18	18	18	18	pF

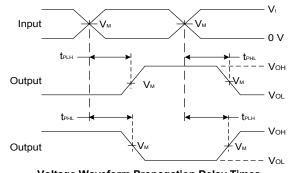


#### **Measurement Information**



Vcc	Inputs		V <sub>M</sub>	CL	RL
VCC	VI	t <sub>r</sub> /t <sub>f</sub>	¥M	OL .	RL.
1.8V ± 0.15V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	30pF	1ΚΩ
2.5V ± 0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	30pF	500Ω
2.7V	V <sub>CC</sub>	≤2.5ns	1.5V	50pF	500Ω
3.3V ± 0.3V	3.0V	≤2.5ns	1.5V	50pF	500Ω
5.0V ± 0.5V	V <sub>CC</sub>	≤2.5ns	V <sub>CC</sub> /2	50pF	500Ω





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

#### Figure 1 Load Circuit and Voltage Waveforms

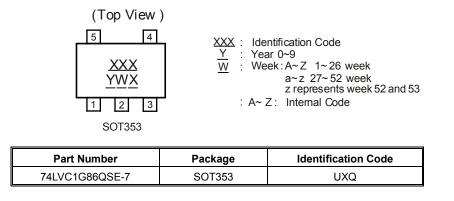
Notes:

- A. Includes test lead and test apparatus capacitance.
  B. All pulses are supplied at pulse repetition rate ≤ 10 MHz.
  C. Inputs are measured separately one transition per measurement.
- D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$ .



# **Marking Information**

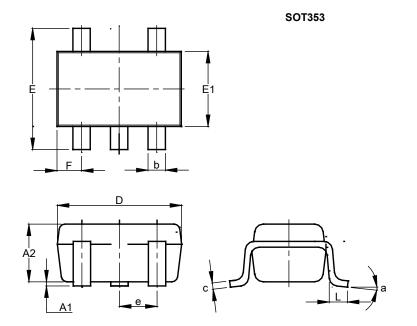
#### SOT353





## **Package Outline Dimensions**

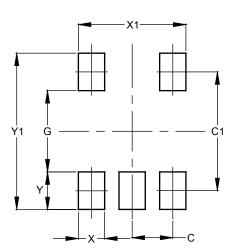
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT353						
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
E	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0	).650 B	SC				
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All	Dimen	sions	in mm				

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Value Dimensions (in mm) С 0.650 C1 1.900 G 1.300 Х 0.420 X1 1.720 Υ 0.600 Y1 2.500

SOT353



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